

**25th IEEE SISC 94**



**San Diego, California**

**Dec 8-10, 1994**

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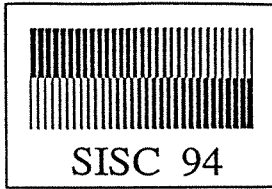
# **ABSTRACTS**

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## Silicon Surface Passivation Control of High Temperature Photoluminescence in Si/Si<sub>1-x</sub>Ge<sub>x</sub>/Si Quantum Wells

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The high temperature ( $T \gg 77\text{K}$ ) band-edge photoluminescence of Si/Si<sub>1-x</sub>Ge<sub>x</sub> structures is of great interest for future Si-based light emitters. For the first time, it is definitively shown through experiment and a model that the intensity of the luminescence of Si/strained Si<sub>1-x</sub>Ge<sub>x</sub>/Si quantum wells at high temperature is controlled, not by the carrier lifetime in Si<sub>1-x</sub>Ge<sub>x</sub>, but by recombination at the top Si surface. By passivating the top Si surface with oxide, the intensity of the high temperature luminescence from the Si<sub>1-x</sub>Ge<sub>x</sub> was increased by nearly an order of magnitude. By employing a high pump power density and an oxide passivated sample, essentially constant Si<sub>1-x</sub>Ge<sub>x</sub> photoluminescence was achieved from 77 to 250K, leading to the most efficient room temperature Si<sub>1-x</sub>Ge<sub>x</sub> photoluminescence yet reported. Furthermore, these results indicate that the temperature dependence of integrated photoluminescence intensity can be used to examine the quality of Si/SiO<sub>2</sub> interfaces.

The samples for this work were grown by Rapid Thermal Chemical Vapor Deposition (RTCVD) using dichlorosilane (DCS) and germane as precursors and at a growth temperature of 625°C. The samples initially consisted of nominally undoped strained single Si<sub>1-x</sub>Ge<sub>x</sub> quantum wells on an undoped Si buffer and capped by 25nm of Si. The surfaces of a piece of each sample were passivated by wet oxidation at 800°C for 10 minutes resulting in about 10nm SiO<sub>2</sub>. Si and Si<sub>1-x</sub>Ge<sub>x</sub> photoluminescence from these samples was measured from 6 to 300K.

A model for the temperature dependence of the integrated intensity of the Si and Si<sub>1-x</sub>Ge<sub>x</sub> photoluminescence was developed. The model assumes that the quasi-fermi levels are flat and that all carrier lifetimes are independent of temperature. At low temperature, nearly all carriers generated by the pump are confined to the Si<sub>1-x</sub>Ge<sub>x</sub> quantum well and therefore the luminescence intensity depends on properties of the Si<sub>1-x</sub>Ge<sub>x</sub> and is constant with temperature. At higher temperatures, as carriers are thermally excited from the quantum well into the Si cladding they are exposed to the top Si surface. If the Si has a low effective carrier lifetime ( $\tau_{\text{Si}}$ ) due to surface recombination, the Si<sub>1-x</sub>Ge<sub>x</sub> luminescence will decay sharply, not because the fraction of carriers in the well decreased but because the total number of carriers in the system decreased. The position of the "knee" in an Arrhenius plot of integrated intensity is determined by  $\tau_{\text{Si}}$ . That  $\tau_{\text{Si}}$  appears to increase by more than an order of magnitude at high pump power density is not well understood.

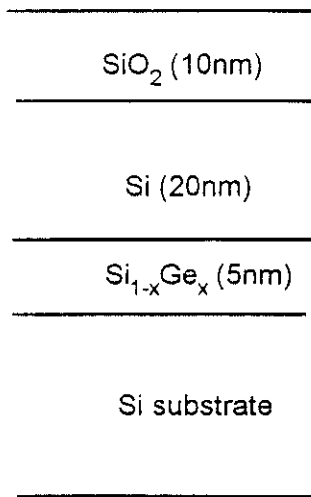


Figure 1. Structure of passivated sample.

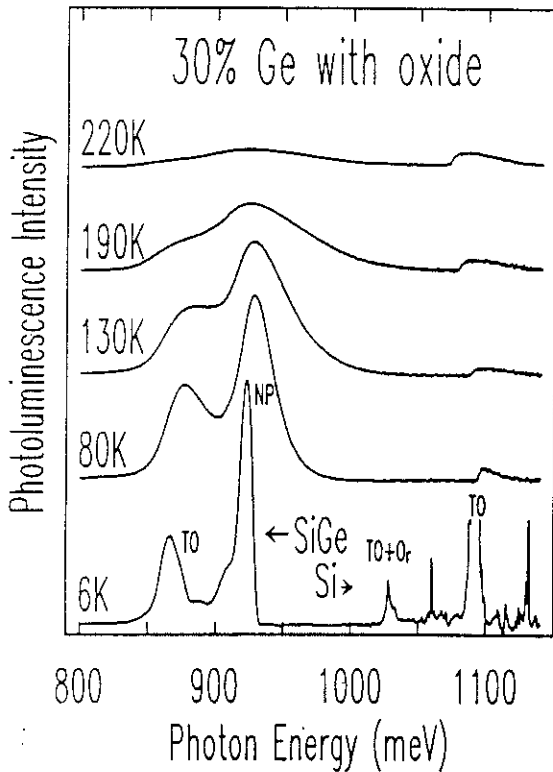


Figure 2. Photoluminescence spectra of the sample with 30% Ge and oxide passivation. The pump power density was  $0.1\text{W}/\text{cm}^2$ . Note that in the 6K spectrum the Si peak is cut off so as not to obscure other spectra.

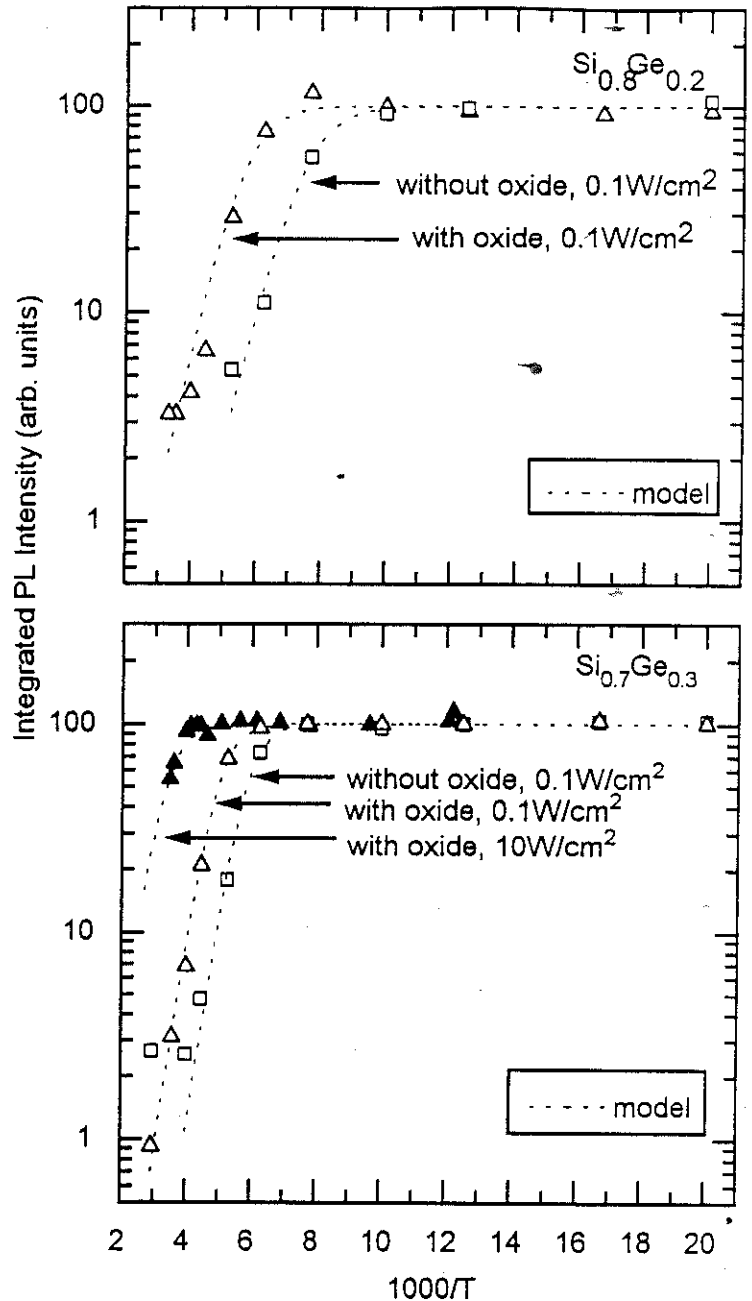


Figure 3. Integrated photoluminescence intensity from  $\text{Si}_{1-x}\text{Ge}_x$  alloy as a function of inverse temperature. The position of the "knee" is determined by the effective carrier lifetime in the Si cladding. Higher temperature of onset of roll-off means higher lifetime.